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Bib Data Sheet

CONFIRMATION NO. 6955

SERIAL NUMBER 10/759,107	FILING DATE 01/20/2004 RULE	CLASS 438	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. M4065.0492/P492-A
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APPLICANTS

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** CONTINUING DATA ***** *JMJ*

This application is a DIV of 10/164,611 06/10/2002 PAT 6,900,521

** FOREIGN APPLICATIONS ***** *JMJ*

None

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/20/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 8	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Louise M. Thomas</i> Examiner's Signature Initials				

ADDRESS

24998
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TITLE

Output prediction logic circuits with ultra-thin vertical transistors and methods of formation

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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